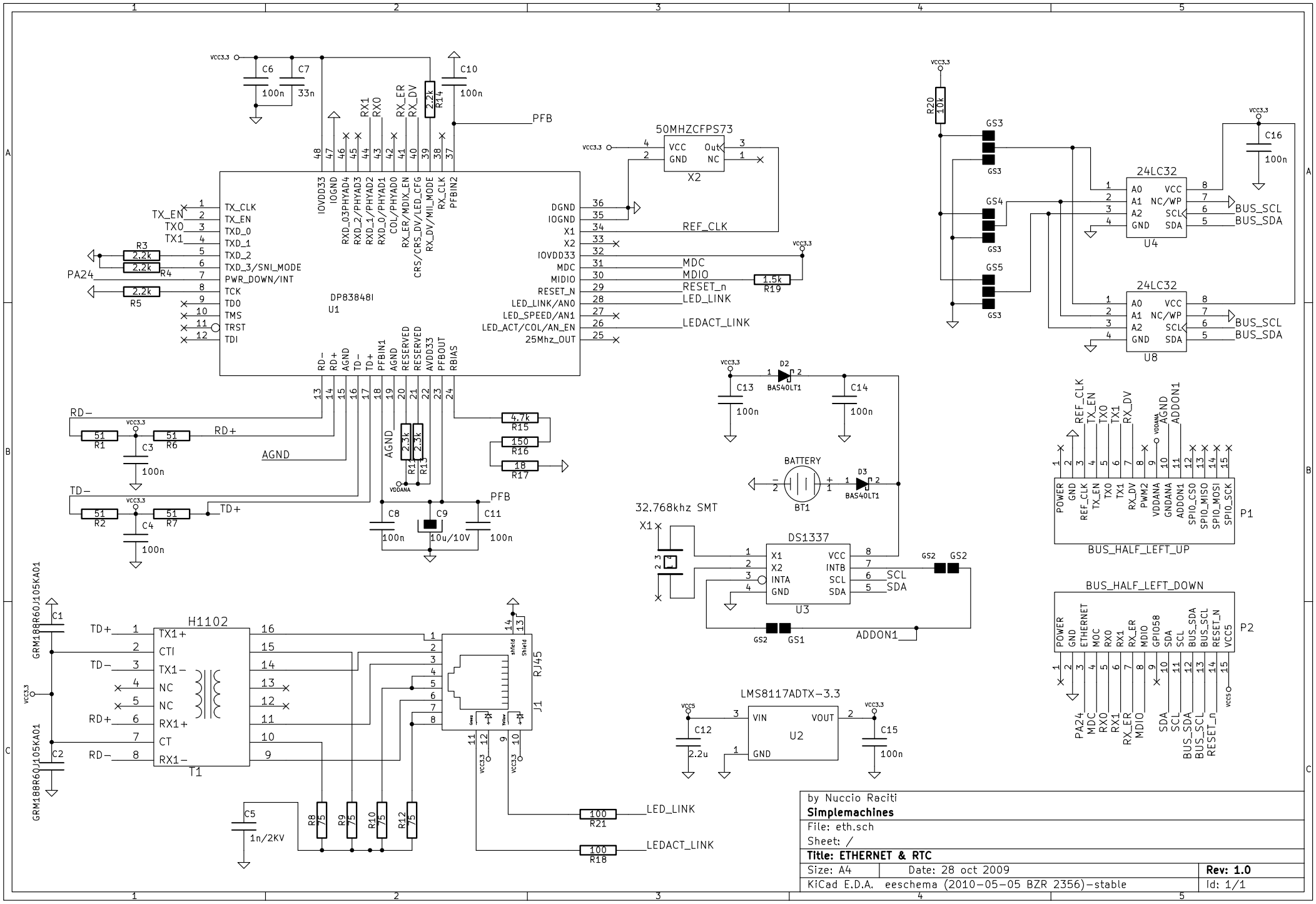
DP83848I
U1
50MHZCFPS73
X2
24LC32
U4
U8
BUS_SCL
BUS_SDA
BUS_HALF_LEFT_UP
P1
BUS_HALF_LEFT_DOWN
P2
BATTERY
BT1
BAS40LT1
32.768khz SMT
DS1337
U3
H1102
T1
RJ45
J1
LMS8117ADTX-3.3
U2
LED_LINK
LEDACT_LINK
PFB
PA24
REF_CLK
MDC
MDIO
RESET_n
ADDON1
by Nuccio Raciti
Simplemachines
File: eth.sch
Sheet: /
Title: ETHERNET & RTC
Size: A4
Date: 28 oct 2009
Rev: 1.0
KiCad E.D.A. eeschema (2010-05-05 BZR 2356)-stable
Id: 1/1